

**DECLARATION (37 CFR 1.63) FOR UTILITY OR DESIGN APPLICATION USING AN
APPLICATION DATA SHEET (37 CFR 1.76)**

Title of Invention	TECHNIQUES FOR PROVIDING MULTIPLE TERMINATION IMPEDANCE VALUES TO I/O PINS ON AN INTEGRATED CIRCUIT
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As the below named inventor(s), I/we declare that:

This declaration is directed to:

- ☒ The attached application, or
☐ Application No. , filed on ,
☐ as amended on ____ (if applicable);

I/we believe that I/we am/are the original and first inventor(s) of the subject matter which is claimed and for which a patent is sought;

I/we have reviewed and understand the contents of the above-identified application, including the claims, as amended by any amendment specifically referred to above;

I/we acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me/us to be material to patentability as defined in 37 CFR 1.56, including for continuation-in-part applications, material information which became available between the filing date of the prior application and the national or PCT International filing date of the continuation-in-part application.

All statements made herein of my/our own knowledge are true, all statements made herein on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001, and may jeopardize the validity of the application or any patent issuing thereon.

FULL NAME OF INVENTOR(S)

Inventor 1 Xiaobao Wang Date: 3/4/04

Signature: Xiaobao Wang Citizen of: Peoples Republic of China

Inventor 2 Chiakang Sung Date: 3/5/04

Signature: Chiakang Sung Citizen of: United States

Inventor 3 _____ Date: _____

Signature: _____ Citizen of: _____

Inventor 4 _____ Date: _____

Signature: _____ Citizen of: _____

☐ Additional inventors are being named on additional form(s) attached hereto.